

# SPL 2007

## III SOUTHERN CONFERENCE ON PROGRAMMABLE LOGIC

**Mar del Plata, Argentina**  
**February 26 - 28, 2007**

SPL is the austral meeting point for researches and industries interested on FPGA technology.

The conference takes place every austral summer in Mar del Plata, a half-million inhabitants seaside resort situated 400 km south of Buenos Aires, Argentina. The venue will be at the unique atmosphere of the one-hundred year old CAECE University building.



**IEEE**

Accepted papers will appear in the IEEE Xplore electronic library, which provides excellent visibility and accessibility to its contents.

***Paper Submission Deadline: October 23, 2006***



The scope of the conference is the design of FPGA-based systems. The topics of interest include, but are not limited to:

### **Design Methodology**

Low-Power Design  
High-speed Techniques  
Reconfiguration  
Fault Tolerant Systems  
Test Techniques  
Failure Analysis and Reliability  
Noise, Radiation Effects and EMC  
Biologically inspired systems  
Physical Design  
Synchronization and Seft-timed Systems

### **FPGA in Education**

Educational Experiences  
Laboratory Materials

### **Platform-based design**

Embedded Processors  
Custom Computers  
IP Cores  
Java, Handel-C, System-C  
Hardware/Software Co-design  
Benchmarking

### **Applications**

Robotics.  
Telecommunications  
Adaptable Systems  
Artificial vision  
Cryptography

### **Signal Processing**

Computer Arithmetics  
Custom DSPs  
Digital Signal Processing  
Software Radio

### **EDA Tools**

Logic and Architectural  
Synthesis  
Modelling and Simulation  
Emulation  
Optimization  
Technology mapping  
System-level partitioning  
Formal Methods in  
System Design



**Organization:**  
School of Engineering  
Universidad Autónoma de Madrid, Spain

<http://www.splconf.org>

