

# 4th SOUTHERN CONFERENCE ON PROGRAMMABLE LOGIC

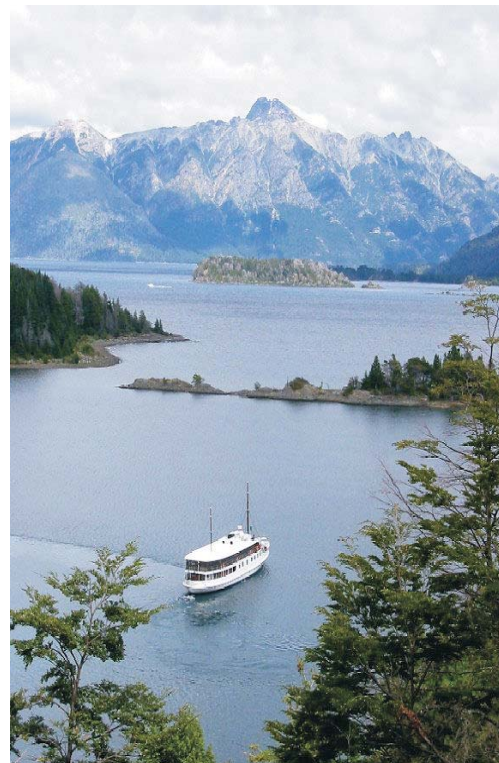
San Carlos de Bariloche, Patagonia Argentina

March 26-28, 2008

**The austral meeting point for researchers interested on FPGA technology**

Computer Vision and Video Processing  
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SPL 2008 edition include a special session devoted to Programmable Logic in High Reliability Applications (spacecraft, aircraft, explosive controls, nuclear power plants, railroad switching devices, life support electronics, and other critical situations)



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Important Dates:

Paper Submission Deadline: November 1, 2007

Designer Forum Deadline: November 1, 2007

Notification of Acceptance: December 4, 2007

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