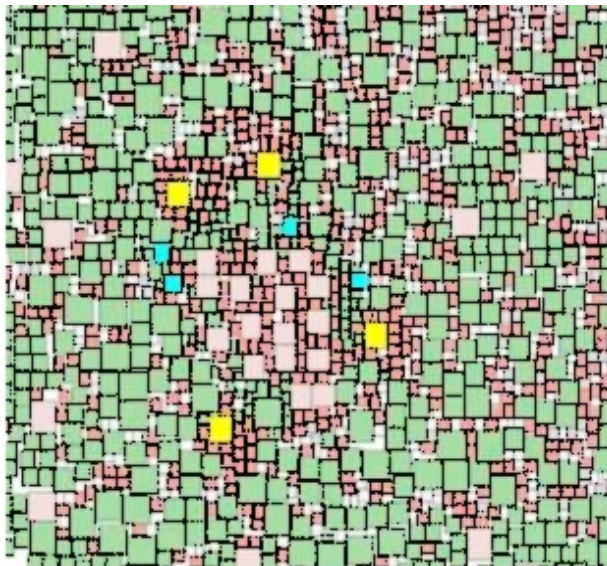




V Southern Programmable Logic Conference

April 1 to 3, 2009 - São Carlos - Brazil

The austral meeting point for researchers interested on **FPGA** technology



- **Design Methodology**
- **Platform-based Design**
- **Applications**
- **Signal Processing**
- **FPGA in Education**
- **EDA Tools**
- **High Reliability embedded Applications**

Important dates

Submission Deadline: Closed

Notification of Acceptance: December 26, 2008

Final version Deadline: January 26, 2009

Early Registration: January 26, 2009



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