www.splconf.org/spl10

General Chairman
Edval J. P. Santos - Universidade Federal de Pernambuco, Brazil

Program Committee Chairmen
•Horacio Neto - INESC-ID, Portugal
•Elías Todorovich - UNICEN, Argentina
Designer Forum Chairmen
•Cristiano Araujo - Universidade Federal de Pernambuco, Brazil
•Valentin Obac Roda - Universidade de São Paulo, Brazil
Europe Region Liaison
•Eduardo Boemo - Universidad Autónoma de Madrid, Spain
•Gustavo Sutter - Universidad Autónoma de Madrid, Spain
•Reiner Hartenstein - Technische Universität Kaiserlautern, Germany
Industry Liaison
•Marilia Lima - Silicon Reef Design House
•João Batista Martins – Universidade Federal de Santa Maria, Brazil
Local Committee
•Alberto C. Mesquita Jr - Universidade Federal de Pernambuco, Brazil
•Cristiano Araujo - Universidade Federal de Pernambuco, Brazil
•Edna Barros - Universidade Federal de Pernambuco, Brazil
•Odilon M. C. Pereira-Filho - Universidade Federal de Pernambuco, Brazil

Program Committee
•Ali Akoglu - University of Arizona, USA
•Altamiro Susin - Universidade Federal do Rio Grande do Sul, Brazil
•Carlos Valderrama - Faculté Polytechnique de Mons, Belgium
•Carol Marsh - Algotronix, UK
•Cesar Ortega-Sanchez - Curtin University of Technology, Australia
•Cristiano Araujo - Universidade Federal de Pernambuco, Brazil
•Debatosh Debnath - Oakland University, USA
•Dominique Lavenier - IRISA / CNRS, France
•Edna Barros - Universidade Federal de Pernambuco, Brazil
•Fadi Aloul - American University of Sarjah, Sarjah
•Guillermo Jaquenod - Universidad Nacional del Centro de la Provincia de Buenos Aires, Argentina
•Guillermo Güichal - UTN, Argentina
•Gustavo Sutter - Universidad Autonoma de Madrid, Spain
•Javier Valls Coquillat - Universidad Politecnica de Valencia, Spain
•João Batista Martins – Universidade Federal de Santa Maria, Brazil
•João M. P. Cardoso - Universidade do Porto, Portugal
•Jorge Luis e Silva - University of Sao Paulo-USP , Brazil
•Jorge M. Finochietto - Politecnico di Torino, Italy
•José Hiroki Saito - Universidade Federal de São Carlos, Brazil
•Juan Pablo Oliver - Universidad de la República, Uruguay
•Julio Perez Acle - Universidad de la República, Uruguay
•Konstantinos Masselos - University of Peloponnese, Greece
•Luciano Agostini - Universidade Federal de Pelotas, Brazil
•Marco D. Santambrogio - Politecnico di Milano, Italy
•Maria Jose Moure - Universidad de Vigo, Spain
•Maximiliano O. Sonnaillon - Balseiro Institute, National University of Cuyo, Argentina
•Michel Renovell - LIRMM, France
•Nelson Acosta - Universidad Nacional del Centro de la Provincia de Buenos Aires, Argentina
•Norian Marranghello - Universidade Estadual Paulista (Rio Preto), Brazil
•Oswaldo Cadenas - The University of Reading, UK
•Pete Sedcole - Imperial College London, UK
•Philip Leong - University of Sidney, Australia
•Reiner Hartenstein - Technische Universität Kaiserlautern, Germany
•Ricardo Cayssials - Universidad Nacional del Sur, Argentina
•Ricardo Pezzuol Jacobi - Universidade de Brasilia, Brazil
•Rolf Fredi Molz - Universidade de Santa Cruz do Sul, Brazil
•Salvador Tropea - INTI, Argentina
•Salvatore Vitabile - Università di Palermo, Italy
•Saulo Finco - Centro de Tecnologia da Informação Renato Archer - CTI, Brazil
•Scott Chin - University of British Columbia, Canada
•Sergio Cuenca - Universidad de Alicante, Spain
•Sébastien Pillement - IRISA, France
•Seda Ogrenci Memik - Northwestern University, USA
•Valentin Obac Roda - Universidade de São Paulo, Brazil
•Wolfgang Klingauf - Xilinx Corp., USA
•Xiaojun Wang - Airvana Inc., USA
•Yannick Le Moullec - Aalborg University, Denmark

SPL is the austral meeting point for researchers and developers interested on reconfigurable logic technology and its applications. The 6th SPL will be hosted by the Electronics and Systems Department, at the Federal University of Pernambuco.
The conference will take place in the city of Ipojuca, Porto de Galinhas beach, State of Pernambuco, Brazil, from March 24th to 26th, 2010. Porto de Galinhas beach is located 60 km from Recife, capital of Pernambuco state.

Conference Topics

Important Date
Submission deadline: November 15th/2009

Design Methodology
•Low-Power Design
•High-speed Techniques
•Physical Design
•Synchronization and Seft-timed Systems
•Dynamic and run-time reconfiguration
•Reconfigurable embedded systems
•Field-programmable analogue arrays
•Interconnects and NoCs

Platform-based Design
•Embedded Processors
•Custom Computers
•IP Cores
•Java, Handel-C, System-C

Applications
•Robotics
•Artificial vision
•Communications/networking/cryptography
•Bioinformatics
•Application acceleration
•Evolvable and bio-inspired applications
•Rapid prototyping

Signal Processing
•Computer Arithmetics
•Digital Signal Processing
•Software Radio

FPGA in Education
•Roadmap of programmable logic
•Teaching reconfigurable systems
•History and surveys of programmable logic
•Emerging device technologies
•Tutorials

High Reliability Embedded Applications
•Design verification and validation
•Reliability and fault tolerance
•FIT rates analysis
•Qualification process
•Device obsolescence
•High reliability processor cores
•Noise, radiation effects and EMC
•Experiences and lessons learned

EDA Tools
•Logic and Architectural
•Synthesis
•Modelling and Simulation
•Emulation
•Formal Methods in System Design
•CAD for reconfigurable architectures
•System-level design methods
•Testing, verification and benchmarking
•Hardware/software co-design